



EXAMINATION BRANCH

TIME-TABLE

TIME: 10.30 A.M TO 12.30 P.M

M.TECH. I SEM. (RA18 REGULATION – 2018 & 2019 BATCHES) SUPPLEMENTARY EXAMINATIONS, OCTOBER-2020

Date & Day	POWER ELECTRONICS	EMBEDDED SYSTEMS	ELECTRONICS DESIGN TECHNOLOGY	ADVANCED MANUFACTURING SYSTEMS	COMPUTER SCIENCE & ENGINEERING
29-10-2020 THURSDAY	Special Machines	Digital System Design	Digital System Design	Computer Integrated Manufacturing	Advanced Data Structures
02-11-2020 MONDAY	Analysis of Power Electronic Converters	RTL Simulation and Synthesis with PLDs	RTL Simulation and Synthesis with PLDs	Advanced Computer Aided Design & Manufacturing	Machine Learning
04-11-2020 WEDNESDAY	Research Methodology and IPR	Research Methodology and IPR	Research Methodology and IPR	Research Methodology and IPR	Research Methodology and IPR
06-11-2020 FRIDAY	Power Electronic Control of DC Drives	--	Analog Integrated Circuit Design	Advanced Material Technology	Distributed Systems
09-11-2020 MONDAY	--	Embedded C	Embedded C	--	Software Process & Project Management
11-11-2020 WEDNESDAY	HVDC Transmission	ARM Architectures	--	Special Manufacturing Process	--

NOTE: ANY CLASHES / OMISSIONS IN THIS TIME-TABLE MAY BE BROUGHT TO THE NOTICE OF THE UNDERSIGNED.

ADDL. CONTROLLER OF EXAMINATIONS
Dt.: 16.10.2020

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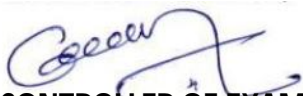
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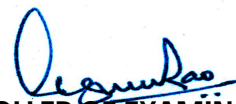
TIME: 10.30 A.M TO 12.30 P.M

M.TECH. I SEM. (RA15 REGULATION – 2015, 2016, 2017 BATCHES) SUPPLEMENTARY EXAMINATIONS, OCTOBER-2020

Date & Day	POWER ELECTRONICS	EMBEDDED SYSTEMS	ELECTRONICS DESIGN TECHNOLOGY	ADVANCED MANUFACTURING SYSTEMS	COMPUTER SCIENCE & ENGINEERING	SOFTWARE ENGINEERING
29-10-2020 THURSDAY	Machine Modelling & Analysis	<u>Elective – II</u> Digital System Design	<u>Elective – I</u> Digital System Design	<u>Elective – II</u> Precision Engineering	Big data and Analytics	Software Requirements and Estimation
02-11-2020 MONDAY	Analysis of Power Electronic Converters	<u>Elective – I</u> VLSI Technology and Design	VLSI Technology and Design	Advanced Computer Aided Design & Manufacturing	Machine Learning	Web Technologies and Services
04-11-2020 WEDNESDAY	Modern Control Theory	Real Time Operating System	CMOS Analog Integrated Circuit Design	Theory of Metal Cutting	<u>Elective – II</u> Software Architecture and Design Patterns	<u>Elective – I</u> Information Security and Audit
06-11-2020 FRIDAY	Power Electronic Control of DC Drives	Embedded System Design	Embedded System Design	<u>Elective – III</u> Applied Tribology	Distributed Systems	Software Development Methodologies
09-11-2020 MONDAY	<u>Elective – II</u> Dynamics of Electrical Machines	<u>Elective – III</u> TCP/IP Networks	<u>Elective – II</u> CPLD and FPGA Architectures and Applications	Automation in Manufacturing	<u>Elective – I</u> Software Process and Project Management	<u>Elective – II</u> Cyber Security and Cyber Laws
11-11-2020 WEDNESDAY	<u>Elective – I</u> High Voltage DC Transmission	ARM Architectures	<u>Elective – III</u> Advanced Data Communications	<u>Elective – I</u> Special Manufacturing Process	Advanced Data Structures and Algorithms	Advanced Data Structures and Algorithms

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EXAMINATION BRANCH

TIME-TABLE

M.TECH. I SEMESTER (RA13 REGULATION – 2013 BATCH) SUPPLE. EXAMINATIONS, OCTOBER - 2020

TIME: 10.30 A.M TO 12.30 P.M

Date & Day	SUBJECTS (RA13 – 2014 Admitted Batch)
	EMBEDDED SYSTEMS
02-11-2020 MONDAY	VLSI TECHNOLOGY AND DESIGN

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EXAMINATION BRANCH

TIME-TABLE

TIME: 10.30 A.M TO 12.30 P.M

M.TECH. II SEM. (RA18 REGULATION – 2018 & 2019 BATCHES) REGULAR & SUPPLE. EXAMINATIONS, OCTOBER-2020

Date & Day	POWER ELECTRONICS	EMBEDDED SYSTEMS	ELECTRONICS DESIGN TECHNOLOGY	ADVANCED MANUFACTURING SYSTEMS	COMPUTER SCIENCE & ENGINEERING
28.10.2020 WEDNESDAY	Power Electronic Control of AC Drives	Embedded OS & RTOS	CMOS VLSI Design	Manufacturing Systems Simulation	Advanced Algorithms
31.10.2020 SATURDAY	Power Electronic Applications to Renewable Energy	--	ASIC and SOC	Theory of Metal Cutting and Forming	Big Data Analytics
03.11.2020 TUESDAY	Microprocessor and Microcontroller	Advanced Digital Signal Processing	Advanced Digital Signal Processing	Advanced Tool Design	Deep Learning
05.11.2020 THURSDAY	--	Embedded Networking	Embedded Networking	NDE & NDT	--
		High Speed Digital Design	High Speed Digital Design		
07.11.2020 SATURDAY	Power Quality	System On Chip Architecture	--	--	Cryptography and Network Security
	Dynamics of Electrical Machines				

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TIME: 10.30 A.M TO 12.30 P.M

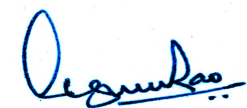
M.TECH. II SEM. (RA15 REGULATION – 2015, 2016, 2017 BATCHES) SUPPLEMENTARY EXAMINATIONS, OCTOBER-2020

Date & Day	POWER ELECTRONICS	EMBEDDED SYSTEMS	ELECTRONICS DESIGN TECHNOLOGY	ADVANCED MANUFACTURING SYSTEMS	COMPUTER SCIENCE & ENGINEERING	SOFTWARE ENGINEERING
28.10.2020 WEDNESDAY	Power Electronic Control of AC Drives	CPLD and FPGA Architectures and Applications	Real Time Operating Systems	Manufacturing Systems Simulation Modelling & Analysis	Advanced Network Programming	Software Process and Project Management
31.10.2020 SATURDAY	Neural And Fuzzy Systems	Hardware Software Co-Design Network Security and Cryptography	CMOS Mixed Signal Circuit Design	Quality Engineering in Manufacturing	High Performance Computing	Big Data and Analytics
03.11.2020 TUESDAY	Microprocessor and Microcontroller	Digital Signal Processors and Architectures	Embedded C Digital Signal Processors and Architectures	Materials Technology	Adhoc and Sensor Networks Database Security	Scripting Languages
05.11.2020 THURSDAY	Flexible AC Transmission Systems (FACTS)	Embedded Networking	Embedded Networking	Production & Operations Management	Web Services and Service Oriented Architecture	Cloud Computing
07.11.2020 SATURDAY	Power Quality	System On Chip Architecture Sensors and Actuators	Ad-hoc Wireless and Sensor Networks Sensors and Actuators	Concurrent Engineering & Product Life Cycle Management	Software Quality Assurance & Testing	Software Quality Assurance and Testing
10.11.2020 TUESDAY	Reliability Engineering	Device Modelling Wireless LANs and PANs	Low Power VLSI Design	Design and Manufacturing of MEMS & Micro Systems	Number theory and Cryptography	Software Architecture and Design Patterns

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EXAMINATION BRANCH

TIME-TABLE

M.TECH. II SEMESTER (RA13 REGULATION – 2014 BATCH) SUPPLE. EXAMINATIONS, OCTOBER - 2020

TIME: 10.30 A.M TO 12.30 P.M

Date & Day	SUBJECTS (RA13 – 2014 Admitted Batch)
	EMBEDDED SYSTEMS
31.10.2020 SATURDAY	HARDWARE SOFTWARE CO- DESIGN
05.11.2020 THURSDAY	EMBEDDED NETWORKING

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EXAMINATION BRANCH

TIME-TABLE

M.TECH. III SEMESTER (RA18 REGULATION – 2018 BATCH) SUPPLE. EXAMINATIONS, OCTOBER - 2020

TIME: 02.30 P.M TO 04.30 P.M

Date & Day	SUBJECTS (RA18 – 2018 Admitted Batch)				
	POWER ELECTRONICS	EMBEDDED SYSTEMS	ELECTRONICS DESIGN TECHNOLOGY	COMPUTER SCIENCE & ENGINEERING	ADVANCED MANUFACTURING SYSTEMS
29-10-2020 THURSDAY	FLEXIBLE AC TRANSMISSION SYSTEMS (FACTS)	DESIGN FOR TESTABILITY	DESIGN FOR TESTABILITY	NATURAL LANGUAGE PROCESSING	MANUFACTURING POLYMERS
02-11-2020 MONDAY	OPERATIONS RESEARCH	OPERATIONS RESEARCH	OPERATIONS RESEARCH	CYBER LAWS	INDUSTRIAL SAFETY

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